

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	160199	reduc\$3 near3 power	US-PGPUB; USPAT	2006/09/04 12:20
2	BRS	L2	822281	processor microprocessor cpu mpu	US-PGPUB; USPAT	2006/09/04 12:21
3	BRS	L3	11094	1 same 2	US-PGPUB; USPAT	2006/09/04 12:21
4	BRS	L4	38998	datapath or (data adj path)	US-PGPUB; USPAT	2006/09/04 12:22
5	BRS	L5	475161	(size width) near5 (chang\$3 alter\$3 reduc\$3)	US-PGPUB; USPAT	2006/09/04 12:23
6	BRS	L6	241327	(size width) near5 (estimat\$3 determin\$3)	US-PGPUB; USPAT	2006/09/04 12:23
7	BRS	L7	1374	(size width) near5 unused	US-PGPUB; USPAT	2006/09/04 12:24
8	BRS	L8	474	4 same 5	US-PGPUB; USPAT	2006/09/04 12:24
9	BRS	L9	315	4 same 6	US-PGPUB; USPAT	2006/09/04 12:25
10	BRS	L10	6	4 same 7	US-PGPUB; USPAT	2006/09/04 12:43
11	BRS	L11	27	3 and 8	US-PGPUB; USPAT	2006/09/04 13:11
12	BRS	L12	6	3 and 9	US-PGPUB; USPAT	2006/09/04 13:36
13	BRS	L13	157728	2 same (queue buffer register)	US-PGPUB; USPAT	2006/09/04 13:37
14	BRS	L14	4972	3 and 13	US-PGPUB; USPAT	2006/09/04 13:37
15	BRS	L15	12634	(queue buffer register) same 5	US-PGPUB; USPAT	2006/09/04 13:38
16	BRS	L16	11805	(queue buffer register) same 6	US-PGPUB; USPAT	2006/09/04 13:39
17	BRS	L17	184	(queue buffer register) same 7	US-PGPUB; USPAT	2006/09/04 13:39
18	BRS	L18	0	14 and 17	US-PGPUB; USPAT	2006/09/04 13:39
19	BRS	L19	330	14 and 16	US-PGPUB; USPAT	2006/09/04 13:40
20	BRS	L20	297	14 and 15	US-PGPUB; USPAT	2006/09/04 13:40
21	BRS	L21	66	19 and 20	US-PGPUB; USPAT	2006/09/04 13:41
22	BRS	L22	1	21 and superscalar	US-PGPUB; USPAT	2006/09/04 13:42
23	BRS	L24	3276	713/300.ccls. 713/320.ccls.	US-PGPUB; USPAT	2006/09/04 13:43

	Type	L #	Hits	Search Text	DBs	Time Stamp
24	BRS	L25	1899	713/322-324.ccls.	US-PGPUB; USPAT	2006/09/04 13:43
25	BRS	L26	4503	24 or 25	US-PGPUB; USPAT	2006/09/04 13:44
26	BRS	L27	0	23 and 26	US-PGPUB; USPAT	2006/09/04 13:44
27	BRS	L23	65	21 not 22	US-PGPUB; USPAT	2006/09/04 13:44